

Date Sheet



The "Huaxin" brand was founded in 2003 and has a history of 18 years. The company is mainly engaged in Hall elements, has a group of senior professional device design, integrated circuit design and test engineers, and has a first-class development and test platform. We have developed a number of high-end products with independent intellectual property rights, such as RF LDMOS series and RF VDMOS series, which represent China's integrated circuit level.

HX6669

Linear Hall Element

HX6669, a linear Hall-effect sensor, is composed of Hall sensor, linear amplifier and Totem-Pole output stage. It features low noise output, which makes it unnecessary to use external filtering. It also can provide increased temperature stability and accuracy. The linear Hall sensor has a wide operating temperature range of -40°C to $+105^{\circ}\text{C}$, appropriate for commercial, consumer, and industrial environments.

The high sensitivity of Hall-effect sensor accurately tracks extremely weak changes in magnetic flux density. The linear sourcing output voltage is set by the supply voltage and in proportion of vary of the magnetic flux density. Typical operation current is 2.5 mA and operating voltage range is 2.8 volts to 6.0 volts. Trim version is available for an ultra low offset products.

The three package styles available provide magnetically optimized solutions for most applications. Package types SO is an SOT-23(1.1 mm nominal height), SQ is an QFN2020-3 (0.5mm nominal height), a miniature low-profile surface-mount package, while package UA is a three-lead ultra-mini SIP for through-hole mounting.

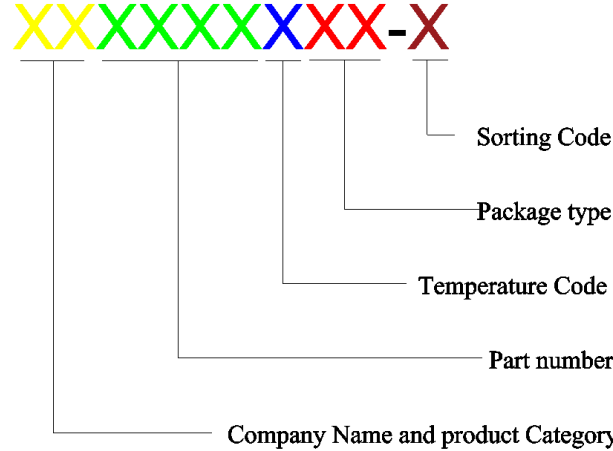
Features and Benefits

- Operating Voltage Range: 2.8V~6.0V
- Power consumption of 3.3mA at 5 V_{DC} for energy efficiency
- Low-Noise Operation
- Linear output for circuit design flexibility
- Totem-Pole for a stable and accurate output
- Responds to either positive or negative gauss
- Magnetically Optimized Package for UA
- Small package for SMD
- Trim version is precise on offset
- Robust ESD performance

Applications

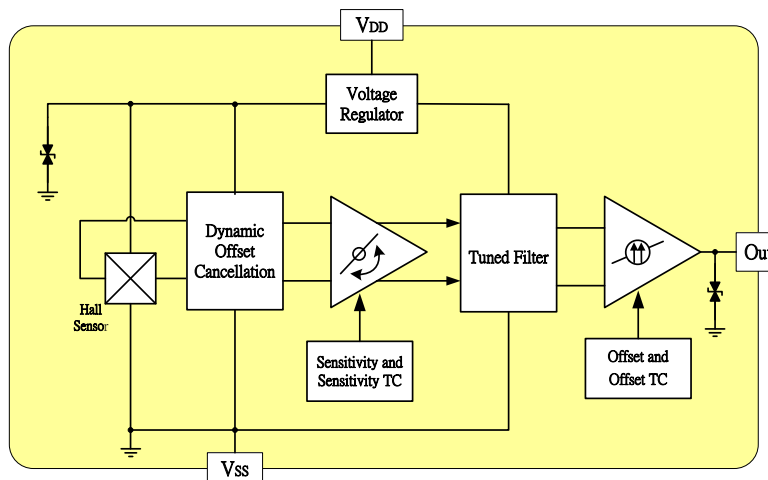
- Current sensing
- Motor control
- Position sensing
- Magnetic code reading
- Rotary encoder
- Ferrous metal detector
- Vibration sensing
- Liquid level sensing
- Weight sensing

Ordering Information

	<p>Company Name and Product Category HX:HX Hall Effect/MP:HX Power IC</p> <p>Part number 6286,6275,6278,6287,6383,6474,6571,6572,6573,6574... If part # is just 3 digits, the fourth digit will be omitted.</p> <p>Temperature range E: 85 °C, I: 105 °C, K: 125 °C, L: 150 °C</p> <p>Package type UA:TO-92S,VK:TO-92S(4pin),VF:TO-92S(5pin),SO:SOT-23, SQ:QFN-3,ST:TSOT-23,SN:SOT-553,SF:SOT-89(5pin), SS:TSOT-26,SD:DFN-6,SG:SOT-89(3pin)</p> <p>Sorting α,β,Blank,....</p>
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Part No.	Temperature Suffix	Package Type
HX6669IUA-A	I (-40°C to +105°C)	UA (TO92-3L)
HX6669IUA-B	I (-40°C to +105°C)	UA (TO92-3L)
HX6669ISQ-A	I (-40°C to +105°C)	SQ (QFN2020-3)
HX6669ISQ-B	I (-40°C to +105°C)	SQ (QFN2020-3)
HX6669ISO-A	I (-40°C to +105°C)	SO(SOT-23)
HX6669ISO-B	I (-40°C to +105°C)	SO(SOT-23)
HX6669IUA-A-T	I (-40°C to +105°C)	SO(SOT-23)
HX6669IUA-B-T	I (-40°C to +105°C)	UA (TO92-3L)
HX6669ISQ-A-T	I (-40°C to +105°C)	UA (TO92-3L)
HX6669ISQ-B-T	I (-40°C to +105°C)	UA (TO92-3L)
HX6669ISO-A-T	I (-40°C to +105°C)	UA (TO92-3L)
HX6669ISO-B-T	I (-40°C to +105°C)	SQ (QFN2020-3)

Functional Diagram



Absolute Maximum Ratings At ($T_a=25^{\circ}\text{C}$)

Characteristics		Values	Unit
Supply Voltage (V_{DD})		8	V
Reverse Voltage, (V_{DDR})		-0.5	V
Output Voltage, (V_{out})		8	V
Output current, (I_{out})		5	mA
Operating Temperature Range, (T_A)		-40 ~ +125	$^{\circ}\text{C}$
Storage temperature Range, (T_S)		-65 ~ +150	$^{\circ}\text{C}$
Thermal Resistance	(θ_{JA}) UA/SO/SQ	206/543/543	$^{\circ}\text{C}/\text{W}$
	(θ_{JC}) UA/SO/SQ	148/410/410	$^{\circ}\text{C}/\text{W}$
Package Power Dissipation, (P_D)		606/230/230	mW

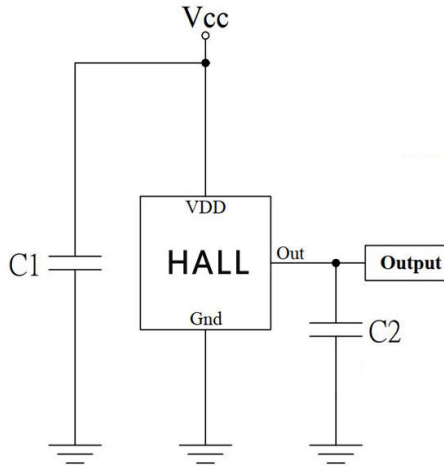
Note: Do not apply reverse voltage to V_{DD} and V_{OUT} Pin, It may be caused for Miss function or damaged device.

Electrical Specifications

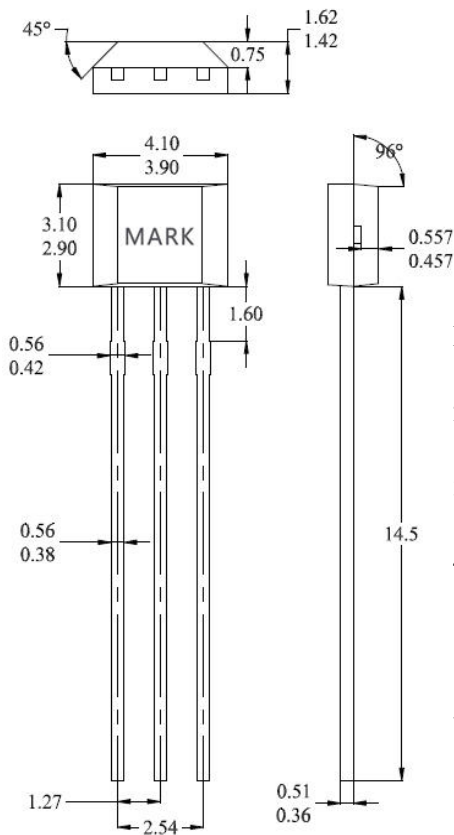
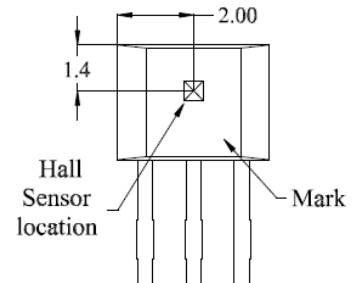
DC Operating Parameters : $T_A=+25^{\circ}\text{C}$, $V_{CC}=5.0\text{V}$

Parameters	Test Conditions		Min	Typ	Max	Units
Supply Voltage, (V_{DD})	Operating		2.8		6.0	V
Supply Current, (I_{DD})	B= 0 Gauss			3.3	5.0	mA
Output Current, (I_O)	$V_{DD} > 3\text{V}$		1.0	1.5		mA
Null Output Voltage, (V_{NULL})	A	B= 0 Gauss, (T Type)	2.35 (2.45)	2.5	2.65 (2.55)	V
	B	B= 0 Gauss, (T Type)	2.325 (2.400)	2.5	2.675 (2.600)	
High Output Voltage, (V_{OH})	B> Max Magnetic Gauss			4.9	4.99	V
Low Output Voltage, (V_{OL})	B> Min Magnetic Gauss		0.01	0.1		V
Output Voltage Span, (V_{OS})				4.8		V
Output Referred Noise, (V_{ON})	$T_a=25^{\circ}\text{C}$, output open			20		mV
Power-On Time, (T_P)					100	μS
Output Switch Time, (T_{SW})					150	μS
Output Switch Frequency, (F_{SW})			3			kHz
Magnetic Range Gauss	A		± 480			Gauss
	B		± 266			Gauss
Ratiometry Null output error, (R_{VON})	Operating voltage range relative to 5V			± 1.5		%
Ratiometry Sensitivity error, (R_{SEN})	Operating voltage range relative to 5V			± 1.5		%
Linearity, (LIN)	% of Span			± 1.5		%
Sensitivity	A	Standard, (T type)	4.6 (4.75)	5.0	5.4 (5.25)	mV/G
	B	Standard, (T type)	8.28 (8.55)	9.0	9.72 (9.45)	mV/G
Sensitivity Temperature Coefficient, (TC_{Sens})	$T_a=105^{\circ}\text{C}$, relative to $Sens@25^{\circ}\text{C}$			± 0.1		$\%/^{\circ}\text{C}$
Delta null voltage, (ΔV_{ON})	$T_a=105^{\circ}\text{C}$, relative to $V_{ON}@25^{\circ}\text{C}$			20		mV

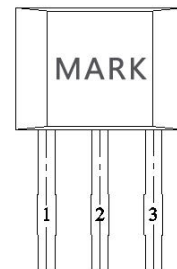
Electro-Static Discharge	HBM	4			KV
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Typical application circuit


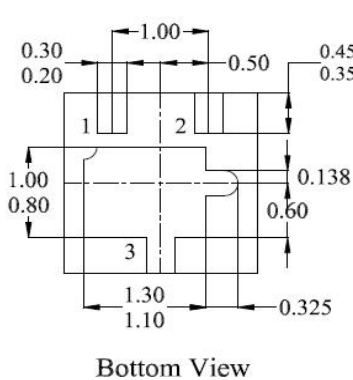
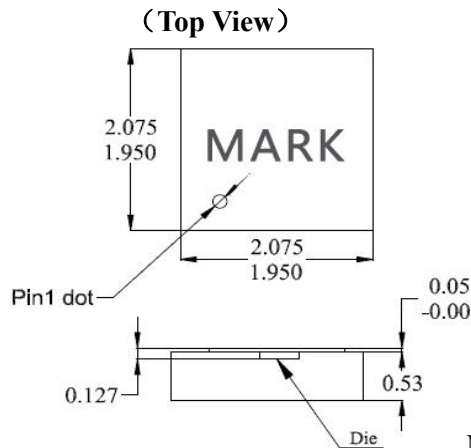
C1 : 1nF/10V
 C2 : 10nF/10V

Sensor Location, Package Dimension and Marking
UA package

Hall Chip Location

NOTES:

- Controlling dimension: mm
- Leads must be free of flash and plating voids
- Do not bend leads within 1 mm of lead to package interface.
- PINOUT:
 Pin 1 VCC
 Pin 2 GND
 Pin 3 Output
- 69XX, 1st X = A/B
 2nd -4th X = Date Code

Output Pin Assignment (Top View)


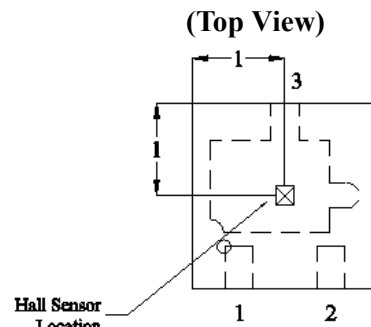
SQ Package



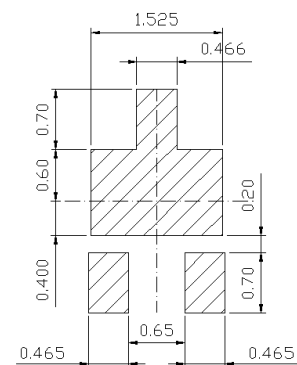
NOTES:

1. PINOUT (See Top View at left)
Pin 1 VDD
Pin 2 Output
Pin 3 GND
2. Controlling dimension: mm;
3. Chip rubbing will be 10mil maximum;
4. Chip must be in PKG. center.
5. 69X, X=A/B
6. XX= Date code

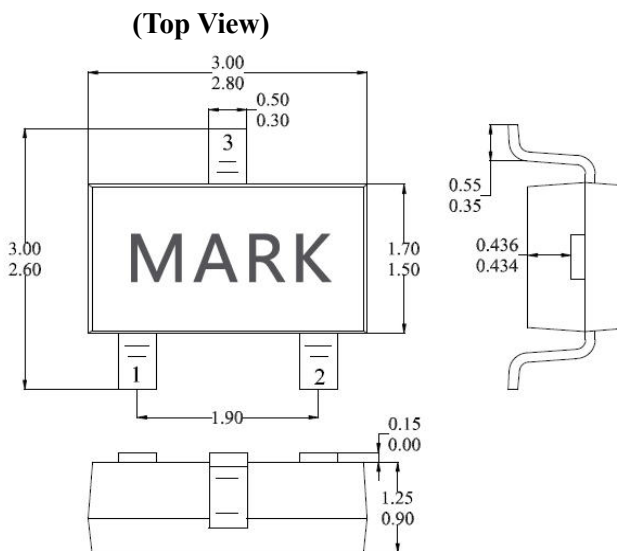
Hall Plate Chip Location



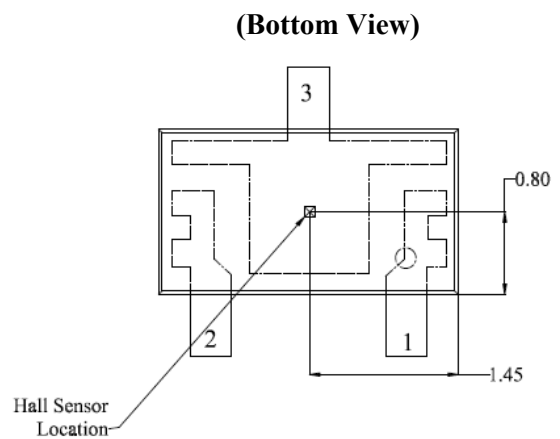
(For reference only) Land Pattern



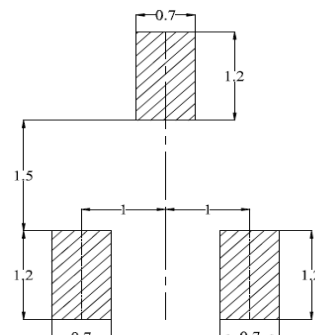
SO Package



Hall Plate Chip Location



(For reference only) Land Pattern



NOTES:

1. Controlling dimension: mm
2. Lead thickness after solder plating will be 0.254mm maximum
3. Chip must be in PKG. center.
4. PINOUT (See Top View at left :)
Pin 1 V_{DD}; Pin 2 Output; Pin 3 GND
5. 69XXX, 1st X = A/B 2nd-3rd = Date Code

Warm reminder

1. Hall is a sensitive device. Please take electrostatic protection measures during use and storage.

2. During the installation process, the Hall should try to avoid applying mechanical stress to the Hall body. If the pins need to be bent, please operate at a distance of 3 mm from the root of the lead.

3. Recommended soldering temperature: soldering with electric soldering iron, the recommended temperature is 350°C, the longest is 5 seconds.

Wave soldering: The recommended maximum temperature is 260°C, the longest is 3 seconds

Infrared reflow soldering: recommended maximum 245°C, maximum 10 seconds

4. It is not recommended to exceed the parameters in the data sheet. Although the Hall will work normally under the limit parameters, it may cause damage to the Hall or the actual product under extreme conditions for a long time. In order to ensure the normal operation of the Hall and the product For safety and stability, please use it within the scope of the data sheet.

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